

Customer No.: 31561  
Docket No.: 13353-US-PA  
Application No.: 10/709823

**In the specification:**

Enclosed please a mark copy and a clean copy of the specification, in which the terms "internal data bus" associated with reference numeral 31 as shown in Fig.3 are amended to "first internal bus," while the terms "internal data bus" associated with reference numeral 51 as shown in Fig.5 are amended to "second internal bus." Additionally, from Fig.4, once the data output latch 415 extracts the nonreal-time data from the bi-directional bus 41, and the control logic unit 411 controls the data output latch 415 when to output the nonreal-time data via the data output bus, not whether to output the nonreal-time data because the latched nonreal-time data must be output in accordance with control of the control logic unit 411. Thus, recitation "and determines whether to output the nonreal-time data via the data output bus or not according to the control of the control logic unit 411" disclosed in line 11 in paragraph [0029] is amended to "and determines when output the nonreal-time data via the data output bus or not according to the control of the control logic unit 411." Since such amendment is supported by Fig.4, it is believed not to introduce new matter. Both mark copy and a clean copy of the specification contain no new matter.